

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Jiang et al.

Serial No.: 10/706,576

Filed: November 12, 2003

For: METHODS'FOR ASSEMBLING MULTIPLE SEMICONDUCTOR DEVICES

Confirmation No.: 6662

Examiner: J. Dolan

Group Art Unit: 2813

Attorney Docket No.: 2269-4886.1US

(01-0201.01/US)

Notice of Allowance Mailed:

<u>September 29, 2006</u>

NOTICE OF EXPRESS MAILING

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AMENDMENT PURSUANT TO 37 C.F.R. § 1.312(a)

Mail Stop ISSUE FEE Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450

Sir:

Please amend the above-referenced application as follows:

Amendments to the specification begin on page 3 of this paper;